

PDCAT08 Schedule

Monday (1 December)				
Time	Castle A (32)	Castle B (32)	Castle C (40)	Castle D (40)
8:00 - 9:00	Registration			
9:00 - 10:30	SeNA mI Workshop Session 1	Tutorial 2A (Passive Network Analysis using Libtrace)	Tutorial 1A (The GreIC Project: Advanced Grid-Database Management)	Tutorial 3A (Multi-core Programming)
10:30 - 11:00	Morning Tea			
11:00 - 12:30	SeNA mI Workshop Session 2	Tutorial 2B (Passive Network Analysis using Libtrace)	Tutorial 1B (The GreIC Project: Advanced Grid-Database Management)	Tutorial 3B (Multi-core Programming)
12:30 - 2:00	Lunch			
2:00 - 3:30	SeNA mI Workshop Session 3		Tutorial 4A (Simulation for Large-Scale Distributed Computing Research)	Tutorial 3C (Multi-core Programming)
3:30 - 4:00	Afternoon Tea			
4:00 - 5:30	SeNA mI Workshop Session 4		Tutorial 4B (Simulation for Large-Scale Distributed Computing Research)	Tutorial 3D (Multi-core Programming)

	Tuesday (2 December)				
Time	Castle A (32)	Castle B (32)	Castle C (40)	Castle D (40)	
8:00 - 9:00	Registration				
9:00 - 10:30	Welcome & Keynote (Castle 1) Looking Toward Exascale Computing <i>Pete Beckman</i>				
10:30 - 11:00	Morning Tea				
11:00 - 12:00	Keynote (Castle 1) Global Experimental Testbeds for Studying Future Internet Technologies <i>Max Ott</i>				
12:00 - 1:30	Lunch				
1:30 - 3:00	PPAM Invited Talk	Formal Languages and Programming Languages (55, 24, 96)	Interconnection Networks (15, 86)	PlanetLab Panel	Session 1
	PPAM Workshop 1				
3:00 - 3:30	Afternoon Tea				
3:30 - 5:30	PPAM Workshop 2	Task Scheduling and Resource Allocation (83, 4, 36, 93)	Grid Computing Systems (92, 72, 44, 104)	PlanetLab Panel	Session 2
6:00 - 8:00	Reception at St. Margaret's College				

	Wednesday (3 December)				
Time	Castle A (32)	Castle B (32)	Castle C (40)	Castle D (40)	
8:00 - 9:00	Registration				
9:00 - 10:00	Keynote (Castle 1) Virtual Organizations by the Rules <i>Carl Kesselman</i>				
10:00 - 10:30	Morning tea				
10:30 - 12:00	HPDataGrid Workshop 1	High Performance Computing (80, 28, 77)	Fault-Tolerance and Reliability (89, 95, 65)	Power-Aware Computing (69, 107)	Session 3
12:00 - 1:30	Lunch				
1:30 - 3:00	HPDataGrid Workshop 2	High Performance Computing (60, 88, 33)	Fault-Tolerance and Reliability (16, 109, 85)	Parallel/Distributed Algorithms (35, 7, 14)	Session 4
3:00 - 3:30	Afternoon tea				
3:30 - 5:30		Computer Networks (29, 97, 37, 17)	Intelligent Computing (48, 51)	Parallel/Distributed Algorithms (2, 34, 59, 53)	Session 5
7:00 - 10:00	Conference Dinner at St. Margaret's College				

	Thursday (4 December)				
Time	Castle A (32)	Castle B (32)	Castle C (40)	Castle D (40)	
8:00 - 9:00	Registration				
9:00 - 10:00	Keynote (Castle 1) Case Studies in Computer Network Measurement <i>Tony McGregor</i>				
10:00 - 10:30	Morning tea				
10:30 - 12:00	Sensor Networks (102, 61, 57)	Algorithms for Cryptographic Systems (18, 56)	Parallel/Distributed Architecture (108, 12, 101)	NZHPC Workshop 1	Session 6
12:00 - 1:30	Lunch				
1:30 - 3:00	Sensor Networks (94, 40)		Parallel/Distributed Architecture (42, 75, 85)	NZHPC Workshop 2	Session 7
3:00 - 3:30	Afternoon tea				
3:30 - 5:00				NZHPC Workshop 3	Session 8